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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/566,454	08/07/2006	2812	1380	1431.146.101/FIN 506 PC*17	23	4

CONFIRMATION NO. 3743

CORRECTED FILING RECEIPT



0000000026496627

25281
DICKE, BILLIG & CZAJA
FIFTH STREET TOWERS
100 SOUTH FIFTH STREET, SUITE 2250
MINNEAPOLIS, MN 55402

Date Mailed: 10/29/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 025281**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/DE04/01360 06/28/2004

Foreign Applications

GERMANY 10334577.9 07/28/2003

If Required, Foreign Filing License Granted: 07/18/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/566,454**

Projected Publication Date: Not Applicable**Non-Publication Request:** No**Early Publication Request:** No

Title

Method for Applying Rewiring to a Panel While Compensating for Position Errors of Semiconductor Chips in Component Positions of the Panel

Preliminary Class

438

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